



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/556,491  
Filing Date .... April 21, 2000  
Inventor .... Scott E. Moore et al.  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 3723  
Examiner .... D. Thomas  
Attorney's Docket No. .... MI22-1422  
Title: "Semiconductor Workpiece Processing Methods"

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**


References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

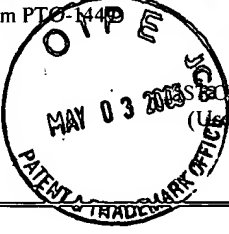
Respectfully submitted,

Dated: 5/2/05

Attorney:

  
James D. Shaurette  
Reg. No. 39,833

EV549572020

Form PT 0-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1422	SERIAL NO. 09/556,491		
 OFFICE OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Scott E. Moore et al.			
				FILING DATE April 21, 2000	GROUP 3723		
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,096,185	8/1/2000	Corlett et al.			
	AB	5,791,970	8/11/1998	Yueh			
	AC	4,282,745	8/1981	Burr, Kenneth J.			
	AD	4,457,624	7/1984	Goldberg et al.			
	AE	5,194,921	3/1993	Tambo et al.			
	AF	3,695,763	10/1972	Shiuh, Jerome			
	AG	5,750,440	5/1998	Vanell et al.			
	AH						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes    No
	AI						
	AJ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AT						
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							